

RESULT LIST

1 result found in the Worldwide database for:

"double buffer" and address and rate and memory in the title or abstract

(Results are sorted by date of upload in database)

‡ APPARATUS FOR PROCESSING THE FLOW OF DIGITAL DATA

Inventor: MCCANN P ; FISHER W

Applicant: US NAVY

EC: G06F5/16; G05B19/408; (+1)

IPC: G05B19/408; G06F5/16; G06F11/00; (+4)

Publication info: US3774165 (A) — 1973-11-20

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Data supplied from the *esp@cenet* database — Worldwide

RESULT LIST

0 results found in the Worldwide database for:

"front buffer" and address and rate and memory in the title or abstract

.....
Data supplied from the *esp@cenet* database — Worldwide

RESULT LIST

0 results found in the Worldwide database for:

"back buffer" and address and rate and memory in the title or abstract

.....
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8 results found in the Worldwide database for:
front and buffer and address and space in the title or abstract
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- | | | | |
|---|--|--|---|
| 1 | Plasma display panel | Inventor: HAN SEONG-HOON [KR] | Applicant: |
| | | EC: H01J17/16; H01J17/49D | IPC: H01J17/49; H01J17/49 |
| | | Publication info: US2007170864 (A1) — 2007-07-26 | |
| 2 | METHOD FOR OPERATING BUFFERS OF DATA LINK LAYER
MANAGEMENT MODULE INSTALLED IN MOBILE
COMMUNICATION TERMINAL | Inventor: KIM GUK JIN | Applicant: PANTECH CO LTD |
| | | EC: | IPC: H04B1/40; H04B1/40; (IPC1-7): H04B1/40 |
| | | Publication info: KR20040033614 (A) — 2004-04-28 | |
| 3 | Input-Output Data Service Computer | Inventor: | Applicant: BURROUGHS CORP |
| | | EC: G06F13/12P | IPC: G06F13/12; G06F13/12 |
| | | Publication info: GB1167762 (A) — 1969-10-22 | |
| 4 | RAM buffer controller for providing simulated first-in-first-
out (FIFO) buffers in a random access memory | Inventor: JOSHI SUNIL P [US]; IYER
VENKATRAMAN [US] | Applicant: ADVANCED MICRO DEVICES INC
[US] |
| | | EC: G06F5/06P | IPC: G06F5/06; G06F5/06; (IPC1-7): G06F12/00;
(+1) |
| | | Publication info: US5133062 (A) — 1992-07-21 | |
| 5 | MULTI-PROCESSOR SYSTEM | Inventor: ECHIGO NOBUYUKI; TANAKA
HIROYUKI | Applicant: HITACHI LTD; HITACHI
INSTRUMENTS ENG |
| | | EC: | IPC: G06F15/167; G06F15/16; G06F15/16; (+1) |
| | | Publication info: JP2141864 (A) — 1990-05-31 | |
| 6 | Addressing arrangement for a RAM buffer controller. | Inventor: JOSHI SUNIL PRADEEP; IYER
VENKATRAMAN | Applicant: ADVANCED MICRO DEVICES INC
[US] |
| | | EC: G06F5/06P | IPC: G11C7/00; G06F3/06; G06F5/06; (+13) |
| | | Publication info: EP0241129 (A2) — 1987-10-14 | |
| 7 | PRINTER-CONTROLLING SYSTEM | Inventor: DAINO TOMOHIRO; KAWAGUCHI
NOBORU (+2) | Applicant: FUJI XEROX CO LTD |
| | | EC: G06K15/12G | IPC: G06F3/12; B41J2/485; B41J2/505; (+10) |
| | | Publication info: JP61100473 (A) — 1986-05-19 | |
| 8 | PRINTING APPARATUS | Inventor: TOKUDA TOSHIHIKO; SHIONO
KAZUHIKO | Applicant: SANYO ELECTRIC CO |
| | | EC: B41J5/30 | IPC: G06F3/12; B41J5/30; B41J19/18; (+7) |
| | | Publication info: JP61044663 (A) — 1986-03-04 | |

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RESULT LIST

15 results found in the Worldwide database for:
double and buffer and address and space in the title or abstract
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- 1 DATA PROCESSING DEVICE AND MEMORY ALLOCATION
METHOD IN DATA PROCESSING DEVICE
Inventor: SAKIMURA TAKEO ; OGAWA TAKESHI
Applicant: CANON KK
EC: IPC: G06F12/08; G06F12/08
Publication info: JP2006127251 (A) — 2006-05-18
- 2 FIFO data buffering method and full up space accessing
FIFO memory
Inventor: YANG ZHU LIU [CN]
Applicant: VIMICRO CORP BEIJING [CN]
EC: IPC: G11C7/00; G11C7/00
Publication info: CN1731529 (A) — 2006-02-08
- 3 INFINIBAND CHANNEL ADAPTER FOR PERFORMING DIRECT
DMA BETWEEN PCI BUS AND INFINIBAND LINK
Inventor: PETTEY CHRISTOPHER J ; RUBIN LAWRENCE H
Applicant: BANDERACOM INC [US]
EC: G06F13/28; G06F13/38A2; (+1) IPC: G06F13/28; G06F13/38; G06F13/40; (+6)
Publication info: WO0235367 (A1) — 2002-05-02
- 4 MESSAGE RECEPTION SYSTEM
Inventor: YAMANE YOSHINORI ; PATRICK HAMILTON
Applicant: HITACHI LTD
EC: IPC: G06F15/16; G06F5/06; G06F15/163; (+5)
Publication info: JP10269191 (A) — 1998-10-09
- 5 Interprocess communication
Inventor: IWASAKI MASAOKI ; CHIBA HIROYUKI (+4)
Applicant: HITACHI LTD [JP]
EC: G06F9/46R6F IPC: G06F9/46; G06F9/46; (IPC1-7): G06F13/38; (+1)
Publication info: GB2295473 (A) — 1996-05-29
- 6 INPUT/ OUTPUT CONVERSION SEGMENT MEMORY DEVICE
Inventor: WAKAYAMA NOBUHIKO
Applicant: MATSUSHITA ELECTRIC IND CO LTD
EC: IPC: G06F12/00; G06T15/40; G06F12/00; (+3)
Publication info: JP3192479 (A) — 1991-08-22
- 7 INFORMATION PROCESSOR
Inventor: HAGIWARA YOSHIHIRO
Applicant: NIPPON ELECTRIC CO
EC: IPC: G06F13/28; G06F13/20; (IPC1-7): G06F13/28
Publication info: JP2166547 (A) — 1990-06-27
- 8 MEMORY DEVICE FOR DOUBLE ACCESS SYSTEM
Inventor: NAKANO TAKESHI
Applicant: MICRO ENG KK
EC: IPC: G06F12/02; G06F12/00; G06F12/02; (+2)
Publication info: JP62015642 (A) — 1987-01-24
- 9 MEMORY CARD
Inventor: TANAKA NOBUO ; TANIGUCHI
Applicant: FUJITSU LTD

HARUMASA

EC: IPC: *G06F12/06; G11C8/00; G06F12/06; (+3)*
Publication info: JP61272850 (A) — 1986-12-03

10 SERIAL DATA TRANSMITTER

Inventor: UMEZAKI SATORU ; KASUGA TOMOAKI Applicant: TOKYO SHIBAURA ELECTRIC CO
EC: H04L13/00 IPC: *H04L29/02; G08C15/12; H04L13/00; (+7)*
Publication info: JP61048258 (A) — 1986-03-08

11 SERIAL DOT PRINTER

Inventor: SAWANO KENJI Applicant: SEIKO EPSON CORP ; SEIKO EPSON CORP
EC: B41J2/505 IPC: *B41J2/485; B41J2/505; B41J2/51; (+4)*
Publication info: JP61043572 (A) — 1986-03-03

12 BUFFER MECHANISM FOR CONVERSION INDEX

Inventor: ANDOU HIDEAKI ; FUJISAKI EIZOU (+1) Applicant: FUJITSU LTD
EC: IPC: *G06F12/10; G06F12/08; G06F13/00; (+5)*
Publication info: JP56019573 (A) — 1981-02-24

13 DATA PROCESSING SYSTEM

Inventor: Applicant: HONEYWELL INF SYSTEMS
EC: G06F9/48C4; G06F9/455H IPC: *G06F9/455; G06F9/48; G06F15/16; (+5)*
Publication info: GB1464650 (A) — 1977-02-16

14 COMPUTER SYSTEM

Inventor: Applicant: CII HONEYWELL BULL
EC: G06F9/48C4; G06F9/46R2 IPC: *G06F9/46; G06F9/48; G06F12/10; (+3)*
Publication info: GB1491453 (A) — 1977-11-09

15 Electronic computer system

Inventor: Applicant: SPERRY RAND CORP
EC: G06F7/57; G06F1/04; (+6) IPC: *G06F1/04; G06F3/09; G06F7/38; (+13)*
Publication info: GB889431 (A) — 1962-02-14

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Applicant, Title of invention, Abstract --- e.g. computer semiconductor

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One letter word or **Stopwords** are not searchable.

front double back

OR

AND

buffer

AND

AND

space address rate display

AND

AND

Date of publication of application --- e.g. 19980401 - 19980405

-

AND

IPC --- e.g. D01B7/04 A01C11/02

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Applicant, Title of invention, Abstract --- e.g. computer semiconductor

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One letter word or **Stopwords** are not searchable.

memory buffer register cache

OR

AND

address space rate display

AND

AND

AND

AND

Date of publication of application --- e.g. 19980401 - 19980405

-

AND

IPC --- e.g. D01B7/04 A01C11/02

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Search

Stored data

No.	Publication No.	Title
1.	<u>2007 - 213055</u>	METHOD OF TRANSFERRING FRAME DATA USING SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY, METHOD OF TRANSFERRING FRAME DATA TO SOURCE DRIVER, AND TIMING CONTROL MODULE
2.	<u>11 - 283015(1999)</u>	PICTURE COLOR MIXING PROCESSOR
3.	<u>08 - 115061(1996)</u>	DRIVING METHOD FOR LIQUID CRYSTAL DISPLAY DEVICE
4.	<u>03 - 233651(1991)</u>	CACHE CIRCUIT
5.	<u>03 - 116194(1991)</u>	DISPLAY CONTROLLER